

On page 13, line 8, the paragraph should read as follows:

A3
Routing requests are received on bus 32 on the left side of the array. An output link availability mask is received on the right side of the array 32. The output link availability mask is represented by signals RDY0 through RDY12, and is received from buffer 150 as shown in FIG. 5.

On page 13, line 21, the paragraph should read as follows:

A4
FIG. 6 shows a network 200 that includes a plurality of routing switches 210. The switches 200 are connected by links 220. Some the links are cross-links 230. One switch is designated as a "root" switch 240.

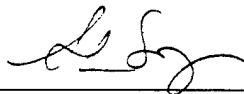
REMARKS

The markup of the amended paragraphs is attached as Exhibit A. The amendment to the specification embodies merely matters of form and does not introduce new matter.

In the event that a fee is due/overpaid, the Commissioner is hereby authorized to charge/credit any such fees to Deposit Account 02-3964 (Order No. 18973-71).

Respectfully submitted,

Dated: May 1, 2002



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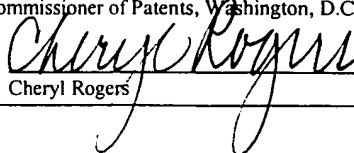
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CERTIFICATE OF MAILING (37 CFR 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to Box No Fee Amendment, Assistant Commissioner of Patents, Washington, D.C. 20231 on:

Date: May 1, 2002


Cheryl Rogers

Serial No. 10/076,820

MARKUP OF THE AMENDED PARAGRAPHS

On page 7, line 12, the paragraph should read as follows:

In summary, the crossbar has one multiplexer 40 or 42 for directing data or flow commands to each of the input and output link units 20 and 22 respectively.

On page 11, line 10, the paragraph should read as follows:

The output link unit 72, as shown in FIG. 4, consists of a pipeline register 114, a decoder 116, a finite state machine (FSM) 118, and a TAXI transmitter 96. Data from the crossbar is held for one clock cycle in the pipeline register 114 to allow setup of decoder 116, as required by the TAXI timing specifications. Whenever an end of packet command byte is received in the pipeline register 114, the FSM 118 recognizes that command and changes its internal state. Thereafter, if the corresponding output link is not blocked by STOP flow control signals received by the input link unit 60, the FSM 118 then sends out a "link available" signal to the router 18 so that the router will know that this link is available for routing a new packet. The FSM 118 also commands the TAXI [Rx]Tx circuit 96 to send out an end of packet command byte and then commands the TAXI 96 to transmit synchronization bytes until the router 18 reconnects the output link 72 to an input link for transmitting another packet.

On page 13, line 8, the paragraph should read as follows:

Routing requests are received on bus 32 on the left side of the array. An output link availability mask is received on the right side of the array 32. The output link availability mask is represented by signals RDY0 through RDY12, and is received from buffer 150 as shown in FIG. 5.

On page 13, line 21, the paragraph should read as follows:

FIG. 6 shows a network [100]200 that includes a plurality of routing switches [110]210. The switches 200 are connected by links 220. Some the links are cross-links 230. One switch is designated as a "root" switch 240.